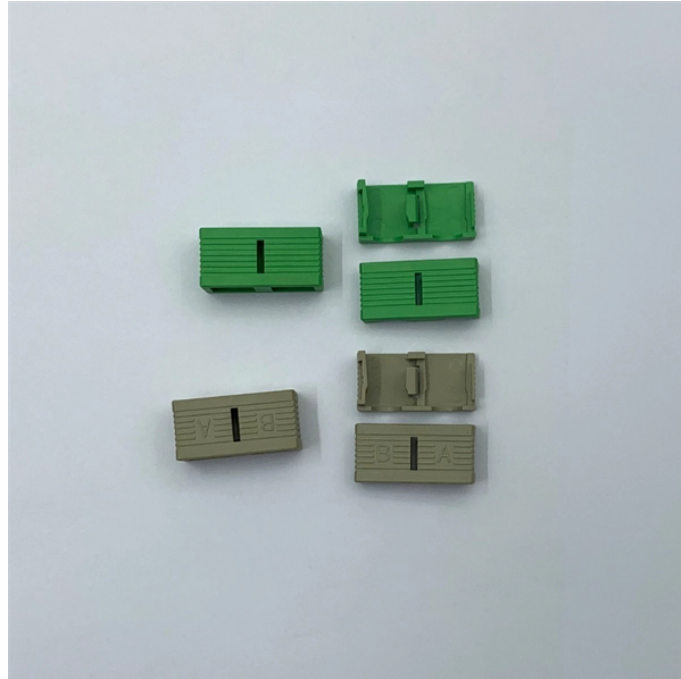


## Brazil Optical Router Low-Loss Customization Process



### Overview

Here, we demonstrate a low-loss, noiseless, polarization-maintaining routing of arbitrarily polarized single photons and, crucially, multi-photon entangled states where the entanglement is encoded in orthogonal polarization bases, at the telecom L-band. Our interferometer-based router is. Over the past decade, our researchers have developed a series of design automation tools for wavelength-routed optical networks-on-chip (WRONoCs): from topology generation to physical design. In our tools, we model the design automation problems as linear and quadratic optimization problems and. This paper is an extended version of our paper published in proceedings of the 2021 IEEE/ACM Asia and South Pacific Design Automation Conference (ASP-DAC), Tokyo, Japan, 18–21 January 2021. Large-scale photonic integrated circuits (LS-PICs) in InP are a critical technology to manage the increasing bandwidth demands of.

## Brazil Optical Router Low-Loss Customization Process



Here, we demonstrate a low-loss, polarization-maintaining router that switches an optical path of arbitrarily polarized heralded single photons and polarization-photon-number-entangled photons at ...



Routing of arbitrarily polarized single photons and polarization-entangled photons is a crucial technology for scaling up quantum information applications. Here, we demonstrate a low-loss, noiseless, ...



Here, we demonstrate a polarization-maintaining electro-optic router compatible with single photons. Our custom electro-optic modulator is embedded in a configuration of a Mach ...



Traditionally, this process is done in two steps: first the logical topology of the network is created according to network requirements (eg. communication matrix), then the elements in the topology are ...



This research aims to present a new route with a minimal amount of optical loss to transfer optical information between source and destination nodes using gray code.



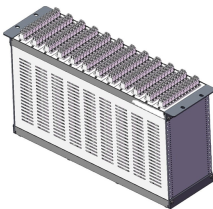
Nowadays in the rapidly evolving field of System on Chip (SoC) technology, the demand for efficient on-chip processing has increased. To address these requireme.



In this paper, a non-blocking five-port photonic router is proposed for the 2-D mesh topology, which is called Surix. Surix has been designed for improving the physical layer and the ...



In this work, we propose a fault-tolerant WRONoC topology, LightR, which provides two independent signal paths for each master-slave pair to tolerate defective MRRs. Moreover, we ...



A novel and highly efficient optical router is introduced, offering improved efficiency and reduced loss in on-chip optical communication and aims to contribute to enhancing on-chip processing capabilities ...



Abstract Large-scale photonic integration depends on robust epitaxial design and fabrication techniques. This paper reviews the integration strategy we developed to demonstrate an 8x8 InP-based ...

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